

Search Terms	
1	ATTENUATED
2	ATTENUATEDS
3	ATTENUATING
4	ATTENUATINGS
5	ATTENUATION
6	ATTENUATIONS
7	ATTENUATOR
8	ATTENUATORS
9	BIT
10	BITS
11	JITTER
12	JITTERS
13	OPTO-ELECTRONIC
14	OPTO-ELECTRONICS
15	OPTOELECTRONIC
16	OPTOELECTRONICS
17	SEQUENCE
18	SEQUENCES
19	SEQUENTIAL
20	SEQUENTIALS
21	TOLERANCE
22	TOLERANCES
23	TOLERATE
24	TOLERATED
25	TOLERATEDS
26	TOLERATES
27	TOLERATING
28	TOLERATINGS
29	TRANSCIEVER
30	TRANSCIEVERS
31	(((((TOLERATING OR TOLERATE OR TOLERATED OR TOLERANCE) SAME (ATTENUATING OR JITTER OR ATTENUATED OR ATTENUATOR OR ATTENUATION))) AND (OPTO-ELECTRONIC OR OPTOELECTRONIC OR TRANSCIEVER))) AND (BITS SAME (SEQUENTIAL OR SEQUENCE)))

	Total	US-PGPUB	USPAT	USOCR	EPO	JPO	Derwent	IBM TDB
1	102771							
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7	45989							
8	14553							
9	674954							
10	376252							
11	37680							
12	3594							
13	15662							
14	1441							
15	23281							
16	6568							
17	1169647							
18	305084							
19	324564							
20	34							
21	234871							
22	181282							
23	41347							
24	87953							
25	0							
26	4421							
27	3782							
28	0							
29	91029							
30	29141							
31	137							

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1			US 20050031097 A1	20050210	131	Gateway with voice	379/93.31
2			US 20040218567 A1	20041104	32	Methods of controlling link quality and transmit power in communication networks	370/332
3			US 20040210790 A1	20041021	26	0.6-2.5 Gbaud CMOS tracked 3X oversampling transceiver with dead zone phase detection for robust clock/data recovery	713/500
4	X		US 20040153267 A1	20040805	22	System and method of testing a transceiver	702/69
5	X		US 20040109476 A1	20040610	31	Clock extracting fabric in a communication device	370/516
6	X		US 20040105516 A1	20040603	19	Digital-data receiver synchronization	375/354
7	X		US 20040090917 A1	20040513	10	Selecting data packets	370/235
8	X		US 20040071095 A1	20040415	10	Quality of service monitor	370/252
9	X		US 20040071086 A1	20040415	17	Traffic congestion	370/230
10	X		US 20040047285 A1	20040311	13	Sub-banded ultra-wideband communications system	370/210
11	X		US 20040019876 A1	20040129	77	Network architecture for intelligent network elements	717/117
12	X		US 20030223526 A1	20031204	8	On-chip system and method for measuring jitter tolerance of a clock and data recovery circuit	375/376
13	X		US 20030223466 A1	20031204	28	Apparatus for redundant multiplexing and remultiplexing of program streams and best effort data	370/537
14	X		US 20030208717 A1	20031106	26	Skew calibration means and a method of skew calibration	714/814
15	X		US 20030206559 A1	20031106	165	Method of determining a start of a transmitted frame in a frame-based communications network	370/509
16	X		US 20030198309 A1	20031023	15	Channel time calibration means	375/354
17	X		US 20030169837 A1	20030911	14	Timing recovery with variable bandwidth phase locked loop and non-linear control paths	375/376
18	X		US 20030156662 A1	20030821	12	Device for the recovery of data from a received data signal	375/340
19	X		US 20030133462 A1	20030717	22	Receiving streams over asynchronous networks	370/395.64

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1			Rabenko, Theodore F. et al.	X							US 20050031097	
2			Budka, Kenneth C. et al.	X							US 20040218567	
3			Moon, Yongsam et al.	X							US 20040210790	
4			Fishman, Alex et al.								US 20040153267	
5			Baba, Takashige et al.								US 20040109476	
6			Smith, Stephen F. et al.								US 20040105516	
7	370/394		Ruutu, Jussi et al.								US 20040090917	
8			Raisanen, Vilho								US 20040071095	
9	370/328; 370/411		Haumont, Serge et al.								US 20040071086	
10			Foerster, Jeffrey R. et al.								US 20040047285	
11	717/171		Dravida, Subrahmanyam et al.								US 20040019876	
12			Sorna, Michael A.								US 20030223526	
13	370/389		Noronha, Ciro Aloisio JR. et al.								US 20030223466	
14			Klotchkov, Ilya Valerievich et al.								US 20030208717	
15	370/463		Trachewsky, Jason Alexander et al.								US 20030206559	
16			Abrosimov, Igor Anatolievich et al.								US 20030198309	
17			Takatori, Hiroshi et al.								US 20030169837	
18	375/376		Engl, Bernhard et al.								US 20030156662	
19			Schoenblum, Joel W.								US 20030133462	

	U	1	Document ID	Issue Date	Pages	Title	Current OR
20	X		US 20030133446 A1	20030717	21	Transmitting streams over asynchronous networks	370/356
21	X		US 20030086515 A1	20030508	196	Channel adaptive equalization precoding system and method	375/346
22	X		US 20030016770 A1	20030123	195	Channel equalization system and method	375/346
23	X		US 20030007473 A1	20030109	42	Method and apparatus for integrating wireless communication and asset location	370/338
24	X		US 20020181633 A1	20021205	121	Means and method for a synchronous network communications system	375/354
25	X		US 20020163932 A1	20021107	167	Method of providing synchronous transport of packets between asynchronous network nodes in a frame-based communications network	370/465
26	X		US 20020138854 A1	20020926		System and method for mapping end user identifiers to access device identifiers	725/147
27	X		US 20020131441 A1	20020919		Method of determining an end of a transmitted frame in a frame-based communications network	370/441
28	X		US 20020131105 A1	20020919		Skew discovery and compensation for WDM fiber communications systems using 8b10b encoding	398/158
29	X		US 20020124111 A1	20020905		System and method for message transmission based on intelligent network element device identifiers	709/246
30	X		US 20020114032 A1	20020822		SPINTRONIC OPTICAL DEVICE	398/65
31	X		US 20020105965 A1	20020808		Broadband system having routing identification based switching	370/463
32	X		US 20020101820 A1	20020801		Broadband system with traffic policing and transmission scheduling	370/229
33	X		US 20020097674 A1	20020725		System and method for call admission control	370/229
34	X		US 20020085589 A1	20020704		System and method for assigning network data packet header	370/503
35	X		US 20020085552 A1	20020704		Broadband system having routing identification assignment	370/389
36	X		US 20020080886 A1	20020627		Method for selecting frame encoding parameters in a frame-based communications network	375/295

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
20			Schoenblum, Joel W.								US 20030133446	
21			Trans, Francois et al.								US 20030086515	
22			Trans, Francois et al.								US 20030016770	
23	370/350		Strong, Jon et al.								US 20030007473	
24			Trans, Francois								US 20020181633	
25	370/389		Fischer, Matthew James et al.								US 20020163932	
26			Desai, Gautam et al.									
27	370/503; 375/343		Trachewsky, Jason Alexander et al.									
28	398/154; 398/161; 398/79		Herrity, Kenneth R.									
29	709/230; 709/249		Desai, Gautam et al.									
30	359/483		Salzman, David B.									
31	370/389; 370/469		Dravida, Subrahmanyam et al.									
32	370/395.4		Gupta, Satya V. et al.									
33	370/252		Balabhadrapatruni, Srinivas et al.									
34	370/473		Dravida, Subrahmanyam et al.									
35	370/254		Tandon, Manas									
36			Ptasinski, Henry S. et al.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
37	X		US 20020078464 A1	20020620		Broadband system with intelligent network devices	725/105
38	X		US 20020075875 A1	20020620		Broadband system with transmission scheduling and flow control	370/395.21
39	X		US 20020075814 A1	20020620		Broadband system with topology discovery	370/255
40	X		US 20020075805 A1	20020620		Broadband system with QOS based packet handling	370/235
41	X		US 20020061012 A1	20020523		Cable modem with voice processing capability	370/352
42	X		US 20020057717 A1	20020516		Method of sharing information among a plurality of stations in a frame-based communications network	370/503
43	X		US 20020057713 A1	20020516		Method for selecting frame encoding parameters to improve transmission performance in a frame-based communications network	370/468
44	X		US 20020044353 A1	20020418		Spintronic optical shutter	359/488
45	X		US 20020042836 A1	20020411		Method of enhancing network transmission on a priority-enabled frame-based communications network	709/232
46	X		US 20020041570 A1	20020411		Method for providing dynamic adjustment of frame encoding parameters in a frame-based communications network	370/252
47	X		US 20020039211 A1	20020404		Variable rate high-speed input and output in optical communication networks	398/9
48	X		US 20020031113 A1	20020314		Extended distribution of ADSL signals	370/352
49	X		US 20020027886 A1	20020307		Method of controlling data sampling clocking of asynchronous network nodes in a frame-based communications network	370/255
50	X		US 20020026523 A1	20020228		Method for distributing sets of collision resolution parameters in a frame-based communications network	709/236
51	X		US 20020012343 A1	20020131		Transceiver method and signal therefor embodied in a carrier wave for a frame-based communications network	370/389
52	X		US 20020006136 A1	20020117		Method for selecting an operating mode for a frame-based communications network	370/466

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
37	706/62		Dravida, Sybrahmanyam et al.									
38	370/395.41; 370/412		Dravida, Subrahmanyam et al.									
39	370/252		Desai, Gautam et al.									
40	370/256		Gupta, Dev V. et al.									
41	725/105		Thi, James C. et al.									
42	370/386		Mallory, Tracy D.									
43	370/253; 370/470; 370/522; 714/776		Bagchi, Amit G. et al.									
44	385/11		Salzman, David B.									
45	709/240		Mallory, Tracy D.									
46	370/389		Ptasinski, Henry S. et al.									
47			Shen, Tian et al.									
48	370/503		Dodds, David E. et al.									
49	370/508		Fischer, Matthew James et al.									
50			Mallory, Tracy D. et al.									
51	370/503		Holloway, John T. et al.									
52	370/389		Mallory, Tracy D. et al.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
53	X		US 20010055311 A1	20011227		Method of determining a collision between a plurality of transmitting stations in a frame-based communications network	370/445
54	X		US 20010038674 A1	20011108		MEANS AND METHOD FOR A SYNCHRONOUS NETWORK COMMUNICATIONS SYSTEM	375/355
55	X		US 20010026387 A1	20011004		Communications network	398/147
56	X		US 20010017557 A1	20010830		Circuit for data signal recovery and clock signal regeneration	327/155
57	X		US 20010016929 A1	20010823		Built-in self test system and method for high speed clock and data recovery circuit	714/735
58	X		US 6834367 B2	20041221		Built-in self test system and method for high speed clock and data recovery circuit	714/738
59	X		US 6820234 B2	20041116		Skew calibration means and a method of skew calibration	714/814
60	X		US 6765931 B1	20040720		Gateway with voice	370/493
61	X		US 6765445 B2	20040720		Digitally-synthesized loop filter circuit particularly useful for a phase locked loop	331/17
62	X		US 6735397 B2	20040511		Skew discovery and compensation for WDM fiber communications systems using 8b10b encoding	398/158
63	X		US 6735396 B2	20040511		Communications network	398/155
64	X		US 6718497 B1	20040406		Method and apparatus for generating jitter test patterns on a high performance serial bus	714/739

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
53	370/462		Trachewsky, Jason Alexander et al.									
54	370/503; 375/371		TRANS, FRANCOIS									
55	359/108		Poustie, Alistair J.									
56			Friedrich, Dirk et al.									
57	714/744; 714/814		Bonneau, Dominique P. et al.									
58	370/249; 370/366; 375/219; 375/224; 714/704; 714/742; 714/814; 714/815		Bonneau; Dominique P. et al.									
59	713/503; 714/700		Deas; Alexander Roger et al.									
60	370/285		Rabenko; Theodore F. et al.									
61	331/11; 331/16; 331/18; 331/25; 375/247; 375/376		Perrott; Michael H. et al.									
62	398/147; 398/155		Herrity; Kenneth R.									
63	398/199; 398/25		Poustie; Alistair J.									
64	370/241; 714/49; 714/821		Whitby-Strevens; Colin									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
65	X		US 6683876 B1	20040127		Packet switched router architecture for providing multiple simultaneous communications	370/395.1
66	X		US 6639939 B1	20031028		Direct sequence spread spectrum method computer-based product apparatus and system tolerant to frequency reference offset	375/140
67	X		US 6624766 B1	20030923		Recovery and transmission of return-to-zero formatted data using non-return-to-zero devices	341/69
68	X		US 6614236 B1	20030902		Cable link integrity detector	324/532
69	X		US 6611537 B1	20030826		Synchronous network for digital media streams	370/503
70	X		US 6606360 B1	20030812		Method and apparatus for receiving data	375/354
71	X		US 6590426 B2	20030708		Digital phase detector circuit and method therefor	327/7
72	X		US 6580376 B2	20030617		Apparatus and method for decimating a digital input signal	341/61
73	X		US 6542555 B2	20030401		Digital transmitter with equalization	375/296
74	X		US 6542460 B1	20030401		Relating to multidirectional communication systems	370/203
75	X		US 6490256 B1	20021203		Method, subscriber device, wireless router, and communication system efficiently utilizing the receive/transmit switching time	370/277
76	X		US 6433599 B1	20020813		Circuit for data signal recovery and clock signal regeneration	327/165
77	X		US 6430148 B1	20020806		Multidirectional communication systems	370/208

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
65	370/401; 370/465; 709/238		Tornes; James E. et al.									
66	375/344; 375/355		Naden; Gary A. et al.									
67	341/56		Possley; Nicholas J. et al.									
68			Karam; Roger A.									
69	370/465		Edens; Glenn T. et al.									
70			Dunning; David S. et al.									
71	327/159; 327/2		Perrott; Michael H.									
72	327/12; 327/147; 327/156		Perrott; Michael H.									
73	333/28R; 375/229		Dally; William J.									
74	370/468		Ring; Steven Richard									
75	370/280; 370/294		Jones; Wesley Stuart et al.									
76	327/156		Friedrich; Dirk et al.									
77	370/203; 370/210; 370/464; 370/503; 370/509; 375/132; 375/354; 375/362; 375/364; 398/79		Ring; Steven Richard									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
78	X		US 6377640 B2	20020423		Means and method for a synchronous network communications system	375/354
79	X		US 6266379 B1	20010724		Digital transmitter with equalization	375/296
80	X		US 6236694 B1	20010522		Bus and interface system for consumer digital equipment	375/363
81	X		US 6215816 B1	20010410		Physical layer interface device	375/219
82	X		US 6108561 A	20000822		Power control of an integrated cellular communications system	455/522
83	X		US 6108314 A	20000822		Method, subscriber device, wireless router, and communication system efficiently utilizing the receive/transmit switching time	370/294
84	X		US 6028462 A	20000222		Tunable delay for very high speed	327/277
85	X		US 5995832 A	19991130		Communications system	455/427
86	X		US 5991824 A	19991123		Method and system for simultaneous high bandwidth input output	710/1
87	X		US 5940753 A	19990817		Controller for cellular communications system	455/422.1
88	X		US 5878329 A	19990302		Power control of an integrated cellular communications system	455/69
89	X		US 5864547 A	19990126		Method and system for controlling uplink power in a high data rate satellite communication system employing on-board demodulation and remodulation	370/318
90	X		US 5841987 A	19981124		Simple bus and interface system for consumer digital equipment	709/236

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
78	370/286; 370/289		Trans; Francois									
79	333/28R; 375/229		Dally; William J.									
80	348/423.1; 375/371		Blatter; Harold et al.									
81	370/402; 375/220; 375/257; 375/258; 375/316		Gillespie; Alan et al.									
82			Mallinckrodt; Albert Jack									
83	370/401		Jones; Wesley Stuart et al.									
84			Kyles; Ian									
85	455/12.1; 455/437; 455/553.1		Mallinckrodt; Albert J.									
86	709/238		Strand; Bradley David et al.									
87	455/12.1; 455/13.1		Mallinckrodt; Albert J.									
88	455/522		Mallinckrodt; Albert Jack									
89	370/527; 455/13.4; 455/69		Strodtbeck; Andrew L. et al.									
90	709/202		Blatter; Harold et al.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
91	X		US 5835857 A	19981110		Position determination for reducing unauthorized use of a communication system	455/410
92	X		US 5832379 A	19981103		Communications system including control means for designating communication between space nodes and surface nodes	455/427
93	X		US 5832030 A	19981103		Multi-carrier transmission system utilizing channels with different error rates	375/260
94	X		US 5831752 A	19981103		Optical packet processing	398/54
95	X		US 5818740 A	19981006		Decimator for use with a modem with time invariant echo path	708/313
96	X		US 5815115 A	19980929		High speed wireless transmitters and receivers	342/359
97	X		US 5812594 A	19980922		Method and apparatus for implementing carrierless amplitude/phase encoding in a network	375/219
98	X		US 5754764 A	19980519		Combination of input output circuitry and local area network systems	709/200
99	X		US 5742642 A	19980421		Signal processing method and apparatus for reducing equalizer error	375/233
100	X		US 5717728 A	19980210		Data/clock recovery circuit	375/355
101	X		US 5682157 A	19971028		Frequency-alternating synchronized infrared	341/68
102	X		US 5608755 A	19970304		Method and apparatus for implementing carrierless amplitude/phase encoding in a network	375/219

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
91	342/387; 342/457; 455/456.5		Otten; David D.									
92	455/12.1; 455/437; 455/553.1		Mallinckrodt; Albert J.									
93	370/480; 375/285; 704/201		Tzannes; Michael A. et al.									
94	370/500; 370/520; 375/354; 398/155; 398/98		Cotter; David et al.									
95	375/222		Agazzi; Oscar Ernesto									
96	455/506; 455/65		Carltoni; Manuel J. et al.									
97	375/298; 375/340; 375/345; 375/355		Rakib; Selim									
98	710/5; 710/56; 711/170; 711/202; 713/500		Davis; Timothy D. et al.									
99	375/348; 375/350; 708/323		Fertner; Antoni									
100			Hein; Jerrell Paul et al.									
101	341/13		Asmussen; Daniel R. et al.									
102	370/201; 375/261		Rakib; Selim									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
103	X		US 5602862 A	19970211		Optical clock extraction	372/45
104	X		US 5555285 A	19960910		Multi-variate system having an intelligent telecommunications interface with automatic adaptive delay distortion equalization (and related method)	379/28
105	X		US 5530842 A	19960625		Generic backplane system which is configurable to serve different network access methods simultaneously	709/221
106	X		US 5515178 A	19960507		Line state transmission system over digital channels for group 3 facsimile	358/445
107	X		US 5499374 A	19960312		Event driven communication network	710/240
108	X		US 5479452 A	19951226		Method and apparatus for aligning a digital receiver	375/344
109	X		US 5452419 A	19950919		Serial communication control system between nodes having predetermined intervals for synchronous communications and mediating asynchronous communications for unused time in the predetermined intervals	709/200
110	X		US 5446756 A	19950829		Integrated cellular communications system	375/130
111	X		US 5421030 A	19950530		Communications system and method for bi-directional communications between an upstream control facility and downstream user terminals	725/106
112	X		US 5400370 A	19950321		All digital high speed algorithmic data recovery method and apparatus using locally generated compensated broad band time rulers and data edge position averaging	375/371
113	X		US 5390351 A	19950214		System for communicating with plural nodes in predetermined intervals depended on integers assigned and changed based upon configuration thereof	709/225
114	X		US 5384806 A	19950124		Modem with time-invariant echo path	375/222

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
103	372/46; 372/50		Barnsley; Peter E. et al.									
104	375/232; 379/398; 379/414		Tapia; Javier J. et al.									
105	370/434; 370/445		Abraham; Menachem et al.									
106	358/438; 379/100.17		Dimolitsas; Spiros et al.									
107	710/107		Di Giulio; Peter C. et al.									
108	455/257		Hayes; David J. et al.									
109	700/56; 710/1		Di Giulio; Peter C. et al.									
110	455/423; 455/427		Mallinckrodt; Albert J.									
111	370/346; 370/391; 379/92.03; 725/131		Baran; Paul									
112	375/359; 375/364; 375/376		Guo; Bin									
113	709/242		Di Giulio; Peter C. et al.									
114	375/354		Agazzi; Oscar E.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
115	X		US 5369623 A	19941129		Acoustic pool monitor with sequentially actuated multiple transducers	367/93
116	X		US 5345473 A	19940906		Apparatus for providing two-way communication in underground facilities	375/218
117	X		US 5339330 A	19940816		Integrated cellular communications system	370/320
118	X		US 5301303 A	19940405		Communication system concentrator configurable to different access methods	709/223
119	X		US 5267269 A	19931130		System and method employing predetermined waveforms for transmit equalization	375/296
120	X		US 5193087 A	19930309		Electronic digital cross-connect system having bipolar violation transparency	370/360
121	X		US 5173899 A	19921222		TDMA communications network of transmitting information between a central station and remote stations	370/503
122	X		US 5107273 A	19920421		Adaptive steerable null antenna processor with null indicator	342/417
123	X		US 5086470 A	19920204		Scrambling in digital communications network using a scrambled synchronization signal	380/267
124	X		US 5073900 A	19911217		Integrated cellular communications system	370/320

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc: Displayed	PT
115	340/540; 340/541; 340/568.1; 340/573.6		Zerangue; Frank									
116	343/741; 375/219; 375/334; 455/19; 455/40; 455/82; 455/83		Berg; Antti									
117	370/325; 370/335; 375/130		Mallinckrodt; Albert J.									
118	370/434; 370/465; 370/908		Abraham; Menachem et al.									
119	178/69N		Shih; Cheng-chung et al.									
120	375/238; 375/289		Lichtash; Avi et al.									
121	370/507; 370/509; 370/517		Ballance; John W.									
122	342/375; 342/379		Roberts; Eugene L.									
123	370/510; 380/268; 398/98; 713/400		Ballance; John W.									
124	370/318; 370/335; 375/130; 455/429; 455/430		Mallinckrodt; Albert J.									

	U	1	Document ID	Issue Date	Pages	Title	Current OR
125	X		US 5063595 A	19911105		Optical communications network	370/522
126	X		US 5034967 A	19910723		Metastable-free digital synchronizer with low phase error	375/373
127	X		US 4977593 A	19901211		Optical communications network	380/2
128	X		US 4797898 A	19890110		Method and apparatus for equalization of data transmission system	375/219
129	X		US 4768188 A	19880830		Optical demand assigned local loop communication system	370/434
130	X		US 4736361 A	19880405		Digital switching system with two-directional addressing rams	370/384
131	X		US 4727509 A	19880223		Master/slave system for replicating/formatting flexible magnetic diskettes	360/15
132	X		US 4656621 A	19870407		Digital switching system	370/366
133	X		US 4561443 A	19851231		Coherent inductive communications link for biomedical applications	607/31
134	X		US 4545078 A	19851001		Method and arrangement for controlling a light switch for optical signals	398/166
135	X		US 4298873 A	19811103		Adaptive steerable null antenna processor	342/375
136	X		US 20040153267 A	20040805		Jitter tolerance determination system for optoelectronic transceiver, compares delayed first sequence of bits received by transceiver and second sequence of bits retransmitted by transceiver in order to determine bit error rate	

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125												
	327/141; 375/359; 375/371		Cox; William M. et al.									
126												
	380/256; 398/168; 398/98		Ballance; John W.									
127												
	375/222; 375/230; 375/358		Martinez; Aldo A.									
128												
	370/442; 370/510; 370/516; 370/535		Barnhart; Andrew W. et al.									
129												
	365/190		Cooperman; Michael et al.									
130												
	360/73.03		Johnson; Ronald R. et al.									
131												
	370/384		Cooperman; Michael et al.									
132												
	128/903; 604/65; 607/32		Hogrefe; Arthur F. et al.									
133												
	385/2		Wiedeburg; Klaus									
134												
			Roberts; Eugene L.									
135												
			BANNIKOV, D et al.									
136												

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137	X		US 3284774 A	19661108		Information transfer system	178/3

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137			EUGENE LEONARD et al.									

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14	112							
15	3							

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1	X		US 20040153267 A1	20040805	22	System and method of testing a transceiver	702/69
2	X		US 20040091005 A1	20040513	16	Temperature and jitter compensation controller circuit and method for fiber optics device	372/34
3	X		US 20030169790 A1	20030911		Maintaining desirable performance of optical emitters over temperature variations	372/34

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1			Fishman, Alex et al.								US 20040153267	
2	372/38.08		Hofmeister, Rulolf J. et al.								US 20040091005	
3	372/33		Chieng, Yew-Tai et al.									

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1 **A CMOS multichannel 10-Gb/s transceiver**

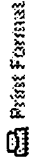
*Takauchi, H.; Tamura, H.; Matsubara, S.; Kibune, M.; Doi, Y.; Chiba, T.; Anbutsu, H.; Yamaguchi, H.; Mori, T.; Takatsu, M.; Gotoh, K.; Sakai, T.; Yamamura, T.; Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 12 , Dec. 2003
Pages:2094 - 2100*

[Abstract] [PDF Full-Text (934 KB)] IEEE JNL

2 **A CMOS 10-gb/s SONET transceiver**

*Muthali, H.S.; Thomas, T.P.; Young, I.A.; Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 7 , July 2004
Pages:1026 - 1033*

[Abstract] [PDF Full-Text (688 KB)] IEEE JNL



3 A quad 0.6-3.2 Gb/s/channel interference-free CMOS transceiver for backplane serial link

Yongsam Moon; Young-Soo Park; Namhoon Kim; Gijung Ahn; Shin, H.J.; Deog-Kyoon Jeong;
Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 5 , May 2004
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[Abstract] [PDF Full-Text (488 KB)] IEEE JNL

4 A 2.5-3.125 Gb/s quad transceiver with second order analog DLL based CDRs

Coban, A.L.; Koroglu, M.H.; Ahmed, K.A.;
Custom Integrated Circuits Conference, 2004. Proceedings of the IEEE 2004 , 3-6
Oct. 2004
Pages:143 - 146

[Abstract] [PDF Full-Text (714 KB)] IEEE CNF

5 Jitter testing for gigabit serial communication transceivers

Yi Cai; Laquai, B.; Luehman, K.;
Design & Test of Computers, IEEE , Volume: 19 , Issue: 1 , Jan.-Feb. 2002
Pages:66 - 74

[Abstract] [PDF Full-Text (601 KB)] IEEE JNL

6 Low-power fully integrated 10-Gb/s SONET/SDH transceiver in 0.13- μm CMOS

Henrickson, L.; Shen, D.; Nellore, U.; Ellis, A.; Joong Oh; Hui Wang; Capriglione, G.; Atesoglu, A.; Yang, A.; Wu, P.; Quadri, S.; Crosbie, D.;
Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 10 , Oct. 2003
Pages:1595 - 1601

[Abstract] [PDF Full-Text (479 KB)] IEEE JNL

7 A hardware efficient 64-QAM low-IF transceiver baseband for broadband communications

Ching-Chi Chang; Muh-Tian Shiue; Chong-Kuang Wang;
Advanced System Integrated Circuits 2004. Proceedings of 2004 IEEE Asia-Pacific Conference on , 4-5 Aug. 2004

Pages:252 - 255

[Abstract] [PDF Full-Text (385 KB)]

8 A 2.5 Gbps - 3.125 Gbps multi-core serial-link transceiver in 0.13 μm /m CMOS

Geurts, T.; Rens, W.; Crols, J.; Kashiwakura, S.; Segawa, Y.; Solid-State Circuits Conference, 2004. ESSCIRC 2004. Proceeding of the 30th European , 21-23 Sept. 2004
Pages:487 - 490

[Abstract] [PDF Full-Text (364 KB)]

9 A complete 400 Mb/s burst-mode data OEIC receiver

Mactaggart, R.; Bendett, M.; Taylor, S.;
Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 1992. Technical Digest
1992., 14th Annual IEEE , 4-7 Oct. 1992
Pages:283 - 286

[Abstract] [PDF Full-Text (200 KB)]

¹⁰ **A 0.6-2.5-GBaud CMOS tracked 3 x oversampling transceiver with dead-zone phase detection for robust clock/data recovery**
Yongsam Moon; Deog-Kyoon Jeong; Gijung Ahn;
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Pages:1974 - 1983

[Abstract] [PDF Full-Text (330 KB)]

11 A single chip 155 Mbps/140 Mbps SDH/PDH transceiver

Guinea, J.; Tomasini, L.; Maggio, S.; Rutar, M.;
Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE
2000 , 21-24 May 2000
Pages:315 - 318

[Abstract] [PDF Full-Text (268 KB)] IEEE CNF

12 Jitter testing for multi-Gigabit backplane SerDes - techniques to decompose and combine various types of jitter

Cai, Y.; Werner, S.A.; Zhang, G.J.; Olsen, M.J.; Brink, R.D.;
Test Conference, 2002. Proceedings. International , 7-10 Oct. 2002
Pages:700 - 709

[Abstract] [PDF Full-Text (554 KB)] IEEE CNF

13 **Multigigabit optically preamplified receiver designs with improved timing tolerance**

Medeiros, C.R.; Fyath, R.S.; O'Reilly, J.J.;
Optoelectronics [see also IEE Proceedings-Optoelectronics], IEE Proceedings
J , Volume: 139 , Issue: 2 , April 1992
Pages:143 - 147

[Abstract] [PDF Full-Text (280 KB)] IEE JNL

14 **New APD-based receivers providing tolerance to alignment jitter for binary optical communications**

O'Reilly, J.J.; Fyath, R.S.;
Optoelectronics [see also IEE Proceedings-Optoelectronics], IEE Proceedings
J , Volume: 135 , Issue: 2 , April 1988
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[Abstract] [PDF Full-Text (464 KB)] IEE JNL

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O'Neill, B.C.; Clark, S.; Wong, K.L.;
Communications Letters, IEEE , Volume: 5 , Issue: 6 , June 2001
Pages:260 - 262

[Abstract] [PDF Full-Text (28 KB)] IEEE JNL

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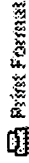
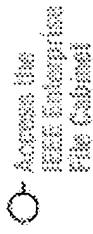
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16 A 5-Gb/s 0.25-/spl mu/m CMOS jitter-tolerant variable-interval oversampling clock/data recovery circuit
Sang-Hyun Lee; Moon-Sang Hwang; Youngdon Choi; Sungjoon Kim; Yongsam Moon; Bong-Joon Lee; Deog-Kyoon Jeong; Wonchan Kim; Young-June Park; Gijung Ahn;

Solid-State Circuits, IEEE Journal of , Volume: 37 , Issue: 12 , Dec. 2002
Pages: 1822 - 1830

[Abstract] [PDF Full-Text (739 KB)] IEEE JNL

17 A 400 Mbps 1394 disk controller IC with integrated DRAM
Killeen, S.; Buckley, F.; Butler, K.; Kiely, T.; Gillen, P.; McCarthy, P.; Fewer, C.; Carter, T.; Moloney, D.; Cahill, C.; Whyte, C.; Harkin, M.; Brosnan, F.; Byrne, G.; Fraisse, V.; Mougeat, P.; Schulze, M.; Moller, G.; Wach, J.;
Systems on a Chip (Ref. No. 1998/439), IEE Colloquium on , 5 Sept. 1998



Pages:15/1 - 15/2

[Abstract] [PDF Full-Text (188 KB)] [IEEE CNF]

18 **All-optical TDM data demultiplexing at 80 Gb/s with significant timing jitter tolerance using a fiber Bragg grating based rectangular pulse switching technology**

Ju Han Lee; Oxenlwe, L.K.; Ibsen, M.; Berg, K.S.; Clausen, A.T.; Richardson, D.J.; Jeppesen, P.;

Lightwave Technology, Journal of, Volume: 21, Issue: 11, Nov. 2003
Pages:2518 - 2523

[Abstract] [PDF Full-Text (470 KB)] [IEEE JNL]

19 **Synchronous and quasisynchronous optical CDMA with balanced complementary receivers**

Gameiro, A.;

Optoelectronics, IEE Proceedings-, Volume: 147, Issue: 5, Oct. 2000
Pages:370 - 376

[Abstract] [PDF Full-Text (508 KB)] [IEEE JNL]

20 **Low latency, ultrafast fiber loop mirror switch with 1.2 ps timing jitter tolerance**

Leng, L.; Koehler, S.D.; Kutz, J.N.; Bergman, K.;

Lasers and Electro-Optics, 1997. CLEO '97., Summaries of Papers Presented at the Conference on, Volume: 11, May 18-23, 1997
Pages:264 - 264

[Abstract] [PDF Full-Text (144 KB)] [IEEE CNF]

21 **A low-power 0.13/spl mu/m CMOS OC-48 SONET and XAUI compliant SERDES**

Wadhwa, R.; Aggarwal, A.; Edwards, J.; Ehler, M.; Hoehn, J.; Miao, G.;

Lakshmikummar, K.; Khoury, J.;

Custom Integrated Circuits Conference, 2003. Proceedings of the IEEE 2003, 21-24 Sept. 2003

Pages:577 - 580

[Abstract] [PDF Full-Text (370 KB)] IEEE CNF

22 FR4 printed circuit board design for Giga-bits embedded optical interconnect applications
Jaemin Shin; Cheolung Cha; Sangycon Cho; Jaehong Kim; Jokerst, N.M.; Brooke, M.;
Electronic Components and Technology, 2004. ECTC '04. Proceedings , Volume: 2 , 1-4 June 2004
Pages:1996 - 2001 Vol.2

[Abstract] [PDF Full-Text (569 KB)] IEEE CNF

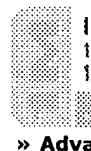
23 A 0.58-1 Gb/s CMOS data recovery circuit using a synchronous digital phase aligner
Cheung, T.S.; Lee, B.C.;
Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Symposium on , Volume: 3 , 4-7 Aug. 2002
Pages:III-385 - III-388 vol.3

[Abstract] [PDF Full-Text (362 KB)] IEEE CNF

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American Control Conference, 2000. Proceedings of the 2000 , Volume: 4 , 28-30 June 2000

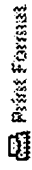
Pages:2523 - 2527 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(388 KB\)\]](#) **IEEE CNF****2 VCSEL to waveguide coupling for optical backplanes***Gwyer, D.; Misselbrook, P.; Philpott, D.; Bailey, C.; Conway, P.; Williams, K.;*

Electronics Packaging Technology, 2003 5th Conference (EPTC 2003) , 10-12 Dec. 2003

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5631759
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5682111
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4361895
5014286
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